Monte Carlo Simulation of Electron Velocity Overshoot in DGSOI MOSFETs

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- Introduction & Aim of the presentation
- Simulation details
- Results
- Conclusions

Introduction

Velocity Overshoot (VO) is an effect observed in very short channel devices.

- Experimentally observed as an increase in current drive and transconductance.
- Transit time is reduced due to lower L_{ch} and also because $v > v_{sat}$.
- Could VO improve the performance of very short channel devices?

Study of VO effect

- Apply low E_{long} for $t < t_0$ and high E_{long} at $t=t_0$. Electric field step \rightarrow Electron velocity overshoot for a time $t < \tau_e$ (energy relaxation time).
- The electron gas is not in equilibrium with the lattice \rightarrow Insufficient phonon scattering events.
- Electrons are accelerated v > v_{sat} until the electron energy reaches its new steady-state.



Goal of the work

Confinement of electrons in a very thin silicon film produces important differences compared with bulk MOSFETs:

- 1. Subband modulation
- 2. Increase in the phonon scattering rate

Try to study the influence of these phenomena on the VO of the electrons and its dependence with technological parameters (T_{Si} , N_{inv})

Simulation Details

- Structure considered: DGSOI transistor with N⁺ poly gates, undoped silicon layer 1.5nm $\leq T_{Si} \leq 20$ nm, $T_{ox} = 2$ nm
- Self consistent solution of Poisson & Schrödinger equations.
- Nonparabolic band model α =0.5eV⁻¹.
- One-electron Monte Carlo simulator.
- Phonon, surface-roughness and Coulomb scattering mechanisms were included.



Evolution of the energy and velocity of a distribution of electrons after the sudden application of a high longitudinal field:



 $\begin{array}{ll} t < 1.5 ps & E = 10^3 \, V/cm \\ t > 1.5 ps & E = 10^5 \, V/cm \end{array}$

Transient velocity overshoot evelocity surpasses the final steadystate value, reaches a maximum and decreases.

$$\tau_{VO} \approx 0.1 ps \rightarrow 0.1 ps \times 10^7 \frac{cm}{s} = 10 nm$$

Electron energy slowly tends to the final value.

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Evolution of the velocity overshoot peak with the silicon thickness (1.5nm $< T_{Si}$ < 20nm) evaluated for different values of N_{inv} .



Two opposite effects influence on the electron transport in DGSOI for very thin silicon thickness:

1.- Subband modulation produces a decrease in the conduction effective mass.

2.- Greater confinement produces an increase in the phonon scattering rate.

Subband modulation effects favor electron transport while the phonon scattering increase impedes it.

Electron mobility in a DGSOI transistor as a function of the silicon thickness



 $T_{Si} > 20$ nm Two separate inversion layers

 $5 \text{nm} < T_{Si} < 20 \text{nm}$ Interaction between inversion layers Subband structure and wavefunctions strongly modified

 $T_{Si} < 5$ nm Strong increase in the phonon scattering rate

<u>Taking into account mobility considerations</u> T_{Si} < 5nm should not be used



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• Behavior of μ_n for T_{Si} < 5nm is mainly controlled by the phonon scattering rate

• The VOP for T_{Si} < 5nm is controlled by the average conduction effective mass

Could VO improve the DGSOI performance? Calculation of I-V curves \rightarrow Ensemble quantum Monte Carlo simulator which includes all the effects previously mentioned.